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## Mpc5748g On78s Mask Set Errata For Mask 0n78s

**mpc5748g On78s, mask set errata for mask 0n78s** - 1. if flexcan is already in freeze mode, go to step 3, otherwise set the halt and frz bits of the can\_mcr register. 2. wait the frzack bit of the can\_mcr register to be set by the hardware. **mpc5748g On78s, mask set errata for mask 0n78s** - this report applies to mask 0n78s for these products: • mpc5748g • mpc5747g • mpc5746g • mpc5748c • mpc5747c mask specific information major mask revision number 2 **devkit-mpc5748g quick start guide - nxp semiconductors** - • latest mpc5748g silicon used (mask set 0n78s). • added microsd connector for sdhc access. • microusb connector replaced with full-size connector for easier use with usb devices. **customer information notification 201702027i - digi-key** - mpc5748g cut 3.0 0n78s is an enhancement to the existing mpc5748g cut2 1n81m revision. this change adds the this change adds the can-fd iso/cd 11898-1: 2014-12-11 support as well as improvements to ethernet, ethernet switch, and low power mode

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